

# CSE 304

# Compiler Design

# Intermediate Code

# Generation II

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# Topics

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- Intermediate Code
  - Translating Expressions
  - Translating Array Elements and Array References
  - Control Flow
  - Boolean Expressions and Short circuiting
  - Avoiding Redundant gotos
  - Backpatching

# Translating Expressions

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- Expression evaluation can be coded by adding 2 attributes to non-terminals comprising expression:
  - `.addr` – Address of result
  - `.code` – Code to generate result
- Add primitive operations to help create intermediate code:
  - **gen()** – This generates an instruction (which is added to `.code`)
  - **newTemp()** – This creates a new temporary register for results
  - Concatenation operator - `||` - This is used to append `.code` attributes and other text for code generation

# Translating Expressions

PRODUCTION	SEMANTIC RULES
$S \rightarrow \text{id} = E ;$	$S.code = E.code \parallel$ $gen(top.get(\text{id.lexeme}) \neq E.addr)$
$E \rightarrow E_1 + E_2$	$E.addr = \text{new Temp}()$ $E.code = E_1.code \parallel E_2.code \parallel$ $gen(E.addr \neq E_1.addr \neq E_2.addr)$
$  - E_1$	$E.addr = \text{new Temp}()$ $E.code = E_1.code \parallel$ $gen(E.addr \neq \text{'minus'} E_1.addr)$
$  ( E_1 )$	$E.addr = E_1.addr$ $E.code = E_1.code$
$  \text{id}$	$E.addr = top.get(\text{id.lexeme})$ $E.code = ''$


$E \rightarrow E_1 + E_2$

1. Generate a new Temporary
2. Append code for  $E_1$   
(generated for some subexpression)
3. Append code for  $E_2$
4. Generate an add instruction
  - a. result field is  $E.addr$
  - b. operands are  $E_1.addr$   
(the temp created for  $E_1$ ) and  $E_2.addr$  (temp created for  $E_2$ )

# Incremental Translation

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- The .code attributes in the previous translation scheme can get very long
  - It is possible to generate the code 'on the fly'
  - Instead of having gen() write the instruction into a code attribute, write it...
    - directly to memory that is holding generated code or...
    - to a file

$$E \rightarrow E_1 + E_2 \quad \{ E.addr = \mathbf{new} \ Temp(); \\ gen(E.addr '=' E_1.addr '+' E_2.addr); \}$$


No code attributes needed since the instructions to compute the values in  $E_1.addr$  and  $E_2.addr$  have already been generated.

# Translating Array References

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- Array elements are stored consecutively in
  - Row-major order
    - Elements across a row are stored in order, then the order moves to the next row
    - Rightmost subscript changes quickest (like a car odometer)
  - Column-major order
    - Consecutive elements move down a column then continue at the top of the next column
    - Left most subscript changes the quickest
- Elements in most languages are numbered 0->n-1 where n is dimension size

# Translating Array References

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- To generate the location (l-value) of an element in a 1 dimensional array:
  - If *base* is the location of element 0
  - *w* is the width of an element in bytes
  - *i* is the element index
  - *Eff addr = base + i \* w*
- Example: A is an array of integers (4 bytes each) and starts at memory location 0x800000. The location of element with index 5 is:
- $0x800000 + 5 * 4 = 0x800014$

# Translating Array References (2 or more dimensions)

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- Given:
  - If  $base$  is the location of element 0
  - $w_r$  is the width of a row in bytes
  - $w_e$  is the width of an element in bytes
  - $i, j$  are the indices for row/element
  - *Eff addr of  $A[i][j] = base + i * w_r + j * w_e$*
  
- Example: A is a 3x5 array of integers at memory location 0x800000:
  - $A[1][2]$  is at  $0x800000 + 1 * 20 + 2 * 4 = 0x80001C$



# Translating Array References (2 or more dimensions)

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- General formula for k-dimensional array:
  - *Eff addr of  $A[i_1][i_2] \dots [i_k] = base + i_1 \times w_1 + i_2 * w_2 + \dots + i_k * w_k$*
- Also, for k dimensions, can use element counts per row/column rather than width.
  - $n_i$  is the number of elements in the row or plane
  - $w$  is the width of the base element
  - base is the base address of the array
- *Eff addr of  $A[i_1][i_2] \dots [i_k] = base + ((\dots(i_1 * n_2 + i_2) * n_3 + i_3) \dots) * n_k + i_k) * w$*

# Semantic Actions for Array References

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```
S → id = E ; { gen( top.get(id.lexeme) != E.addr); }  
  | L = E ; { gen(L.addr.base '[' L.addr ')' != E.addr); }  
E → E1 + E2 { E.addr = new Temp();  
                 gen(E.addr != E1.addr '+' E2.addr); }  
  | id         { E.addr = top.get(id.lexeme); }  
  | L         { E.addr = new Temp();  
                 gen(E.addr != L.array.base '[' L.addr ')' ); }  
L → id [ E ] { L.array = top.get(id.lexeme);  
              L.type = L.array.type.elem;  
              L.addr = new Temp();  
              gen(L.addr != E.addr '*' L.type.width); }  
  | L1 [ E ] { L.array = L1.array;  
              L.type = L1.type.elem;  
              t = new Temp();  
              L.addr = new Temp();  
              gen(t != E.addr '*' L.type.width); }  
              gen(L.addr != L1.addr '+' t); }
```

# Semantic Actions for Array References

```
S → id = E ; { gen( top.get(id.lexeme) != E.addr); }  
  | L = E ; { gen(L.addr.base '[' L.addr ']' != E.addr); }  
E → E1 + E2 { E.addr = new Temp();  
                gen(E.addr != E1.addr '+' E2.addr); }  
  | id { E.addr = top.get(id.lexeme); }  
  | L { E.addr = new Temp();  
        gen(E.addr != L.array.base '[' L.addr ']); }
```

```
L → id [ E ] { L.array = top.get(id.lexeme);  
              L.type = L.array.type.elem;  
              L.addr = new Temp();  
              gen(L.addr != E.addr '*' L.type.width); }
```

```
| L1 [ E ] { L.array = L1.array;  
             L.type = L1.type.elem;  
             t = new Temp();  
             L.addr = new Temp();  
             gen(t != E.addr '*' L.type.width); }  
             gen(L.addr != L1.addr '+' t); }
```

1. Get symbol info into L<sub>1</sub>.array
2. Get type into L<sub>1</sub>.type.elem
3. L.addr is a new temporary
4. Compute address into L.addr (note: L.type.width is size of whole row, plane, etc)

# Semantic Actions for Array References

```
S → id = E ; { gen( top.get(id.lexeme) != E.addr); }  
  | L = E ; { gen(L.addr.base '[' L.addr ']' != E.addr); }  
  
E → E1 + E2 { E.addr = new Temp();  
                  gen(E.addr != E1.addr '+' E2.addr); }  
  
  | id { E.addr = top.get(id.lexeme); }  
  
  | L { E.addr = new Temp();  
        gen(E.addr != L.array.base '[' L.addr ']); }  
  
L → id [ E ] { L.array = top.get(id.lexeme);  
               L.type = L.array.type.elem;  
               L.addr = new Temp();  
               gen(L.addr != E.addr '*' L.type.width); }
```

```
  | L1 [ E ] { L.array = L1.array;  
                L.type = L1.type.elem;  
                t = new Temp();  
                L.addr = new Temp();  
                gen(t != E.addr '*' L.type.width); }  
                gen(L.addr != L1.addr '+' t); }
```

1. Copy L<sub>1</sub>.array to L.array
2. Get type of subarray (from L<sub>1</sub>.type.elem)
3. Generate a new temp (t)
4. Generate a new temp (L.addr)
5. Create code to calculate t and add it to L.addr (the offset from the base)

# Control Flow

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- Boolean expressions can be used
  - To compute a logical value (true/false)
  - To alter control flow

• Here we are concerned with the latter

• Consider the following grammar:

$B \rightarrow B \mid B \mid B \&\&B \mid !B \mid ( B ) \mid E \text{ rel } E \mid \text{true} \mid \text{false}$

- rel is one of the relational operators (<, <=, ==, !=, >, >=)
- $\mid \mid$  is logical OR,  $\&\&$  is logical AND,  $!$  is NOT

# Control Flow

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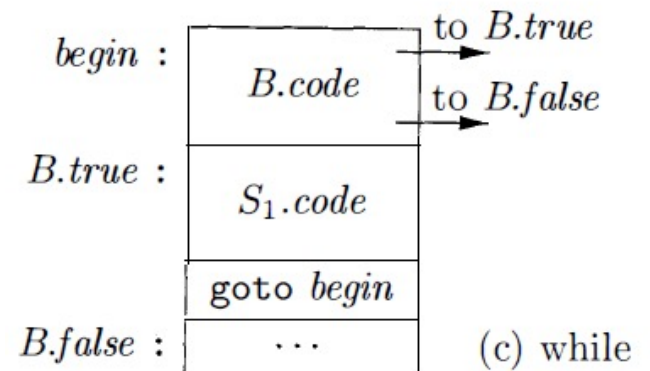
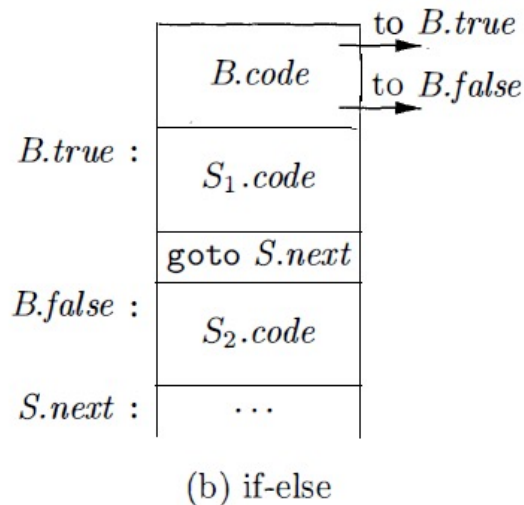
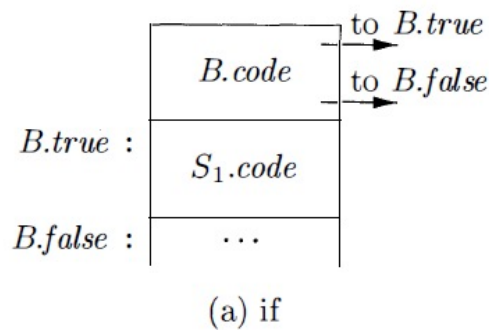
- Depending on language semantics, Boolean expressions may NOT need to be completely evaluated
- Most languages allow ‘short circuit’ evaluation (quit once you know the result)
  - $B_1 \ || \ B_2$  – If  $B_1$  is true, we know the whole expression is true so skip  $B_2$  eval
  - $B_1 \ \&\& \ B_2$  – If  $B_1$  is false, we know expression is false so skip  $B_2$  eval

# Flow of Control Statements

Here is a small grammar of Flow of Control Statements

$S \rightarrow \text{if} ( B ) S_1$        $B$  represents a Boolean expression  
 $S \rightarrow \text{if} ( B ) S_1 \text{ else } S_2$        $S$  represents statements in the language  
 $S \rightarrow \text{while} ( B ) S_1$

Need to create semantic actions that generate the following patterns of code:



# Flow of Control Statements

PRODUCTION	SEMANTIC RULES
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \parallel label(S.next)$
$S \rightarrow \text{assign}$	$S.code = \text{assign.code}$
$S \rightarrow \text{if} ( B ) S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \parallel label(B.true) \parallel S_1.code$
$S \rightarrow \text{if} ( B ) S_1 \text{ else } S_2$	$B.true = newlabel()$ $B.false = newlabel()$ $S_1.next = S_2.next = S.next$ $S.code = B.code$ $\parallel label(B.true) \parallel S_1.code$ $\parallel gen('goto' S.next)$ $\parallel label(B.false) \parallel S_2.code$

In this and following slides:

- $B.true$ ,  $B.false$ ,  $S.next$ ,  $S_1.next$ , etc are labels for branch transfers
- **B.true** – Branch here when B is true
- **B.false** – Branch here when B is false
- **S.next, S<sub>1</sub>.next** – This is the target of the next statement after S, S<sub>1</sub>, etc



# Flow of Control Statements

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PRODUCTION	SEMANTIC RULES
$S \rightarrow \mathbf{while} ( B ) S_1$	$begin = newlabel()$ $B.true = newlabel()$ $B.false = S.next$ $S_1.next = begin$ $S.code = label(begin) \parallel B.code$ $\quad \parallel label(B.true) \parallel S_1.code$ $\quad \parallel gen('goto' begin)$
$S \rightarrow S_1 S_2$	$S_1.next = newlabel()$ $S_2.next = S.next$ $S.code = S_1.code \parallel label(S_1.next) \parallel S_2.code$

# Boolean Expressions

PRODUCTION	SEMANTIC RULES
$B \rightarrow B_1 \    \ B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \    \ label(B_1.false) \    \ B_2.code$
$B \rightarrow B_1 \ \&\& \ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \    \ label(B_1.true) \    \ B_2.code$
$B \rightarrow ! B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$
$B \rightarrow E_1 \ rel \ E_2$	$B.code = E_1.code \    \ E_2.code$ $\    \ gen('if' \ E_1.addr \ rel.op \ E_2.addr \ 'goto' \ B.true)$ $\    \ gen('goto' \ B.false)$
$B \rightarrow true$	$B.code = gen('goto' \ B.true)$
$B \rightarrow false$	$B.code = gen('goto' \ B.false)$

## **B || B**

1. Target true result of  $B_1$  to overall result (short circuit)
2. False result needs a label
3.  $B_2.true$  goes to overall true result target
4.  $B_2.false$  goes to false target of B
5. Code is  $B_1$  eval code, the label  $B_1.false$ , and the  $B_2$  eval code

## **B && B**

Similar to  $B \ || \ B$  but reverse false and true evaluation targets

# Boolean Expressions

PRODUCTION	SEMANTIC RULES
$B \rightarrow B_1 \parallel B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \parallel label(B_1.false) \parallel B_2.code$
$B \rightarrow B_1 \ \&\& \ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \parallel label(B_1.true) \parallel B_2.code$
$B \rightarrow ! B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$
$B \rightarrow E_1 \ \mathbf{rel} \ E_2$	$B.code = E_1.code \parallel E_2.code$ $\parallel gen('if' \ E_1.addr \ \mathbf{rel.op} \ E_2.addr \ 'goto' \ B.true)$ $\parallel gen('goto' \ B.false)$
$B \rightarrow \mathbf{true}$	$B.code = gen('goto' \ B.true)$
$B \rightarrow \mathbf{false}$	$B.code = gen('goto' \ B.false)$

**!B**

Just reverse true and false labels from  $B_1!$

# Avoiding Redundant Gotos

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- Can reduce gotos by clever reorganization of tests and control transfers
- Consider:  $S \rightarrow \text{if } (B) \text{ then } S_1 \Rightarrow$  Actions are:  
B.true = newlabel()  
B.false =  $S_1$ .next = S.next  
B.code || label(B.true) ||  $S_1$ .code

Now, use a new operator ***fall*** meaning *do not generate a goto*

$S \rightarrow \text{if } (B) \text{ then } S_1 \Rightarrow$  actions are now:  
B.true = fall  
B.false =  $S_1$ .next = S.next  
B.code ||  $S_1$ .code

# Avoiding Redundant Gotos

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- Can reduce gotos by clever reorganization of tests and control transfers
- Consider:  $S \rightarrow \text{if } (B) \text{ then } S_1 \text{ else } S_2 \Rightarrow$  actions are:  
B.true = newlabel()  
B.false = newlabel()  
 $S_1.\text{next} = S_2.\text{next} = S.\text{next}$   
 $S.\text{code} = B.\text{code} \parallel \text{label}(B.\text{true}) \parallel S_1.\text{code} \parallel \text{gen}(\text{'goto' } S.\text{next}) \parallel \text{label}(B.\text{false}) \parallel S_2.\text{code}$

Now, use a new operator **fall** meaning *do not generate a goto*

$S \rightarrow \text{if } (B) \text{ then } S_1 \text{ else } S_2 \Rightarrow$  actions are now:  
B.true = fall  
B.false = newlabel()  
S.next = newlabel()  
 $S.\text{code} = B.\text{code} \parallel S_1.\text{code} \parallel \text{gen}(\text{'goto' } S.\text{next}) \parallel \text{label}(B.\text{false}) \parallel S_2.\text{code}$

# Backpatching

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- Generating jump instructions on the fly may require a second pass to determine the address of labels generated.
- **Backpatching** allows 1 pass translation by keeping lists of jump targets created as synthesized attributes
- Need 3 lists:
  - **B.truelist** – instructions that need a target when B is *true*
  - **B.falselist** – instructions that need a target when B is *false*
  - **S.nextlist** – instructions that need to jump to the instruction after the code in S.
- 3 functions are used:
  - **makelist(i)** – Creates a new list containing only i, an index into the instruction list
  - **merge(p1,p2)** – merges two lists and returns the merged lists
  - **backpatch(p, i)** – Patches (fixes) the targets of all conditional and unconditional jumps in the locations found in list p to point at instruction i.

# Backpatching – Boolean Expressions

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- 1)  $B \rightarrow B_1 \parallel M B_2$  { *backpatch*(*B*<sub>1</sub>.*falselist*, *M.instr*);  
*B.truelist* = *merge*(*B*<sub>1</sub>.*truelist*, *B*<sub>2</sub>.*truelist*);  
*B.falselist* = *B*<sub>2</sub>.*falselist*; }
- 2)  $B \rightarrow B_1 \ \&\& \ M \ B_2$  { *backpatch*(*B*<sub>1</sub>.*truelist*, *M.instr*);  
*B.truelist* = *B*<sub>2</sub>.*truelist*;  
*B.falselist* = *merge*(*B*<sub>1</sub>.*falselist*, *B*<sub>2</sub>.*falselist*); }
- 3)  $B \rightarrow ! B_1$  { *B.truelist* = *B*<sub>1</sub>.*falselist*;  
*B.falselist* = *B*<sub>1</sub>.*truelist*; }
- 4)  $B \rightarrow ( B_1 )$  { *B.truelist* = *B*<sub>1</sub>.*truelist*;  
*B.falselist* = *B*<sub>1</sub>.*falselist*; }
- 5)  $B \rightarrow E_1 \ \mathbf{rel} \ E_2$  { *B.truelist* = *makelist*(*nextinstr*);  
*B.falselist* = *makelist*(*nextinstr* + 1);  
*emit*('if' *E*<sub>1</sub>.*addr* *rel.op* *E*<sub>2</sub>.*addr* 'goto -');  
*emit*('goto -'); }
- 6)  $B \rightarrow \mathbf{true}$  { *B.truelist* = *makelist*(*nextinstr*);  
*emit*('goto -'); }
- 7)  $B \rightarrow \mathbf{false}$  { *B.falselist* = *makelist*(*nextinstr*);  
*emit*('goto -'); }
- 8)  $M \rightarrow \epsilon$  { *M.instr* = *nextinstr*; }

Marker non terminal picks up location of next instruction generated (see production 8)

Backpatching done during compound expression evaluation

Lists are either reversed or copied in productions 3 and 4

*makelist*() is needed for productions 5, 6, and 7

# Backpatching – Boolean Expressions

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Consider

```
1)  $B \rightarrow B_1 \parallel M B_2$  { backpatch( $B_1$ .falselist,  $M$ .instr);  
    $B$ .truelist = merge( $B_1$ .truelist,  $B_2$ .truelist);  
    $B$ .falselist =  $B_2$ .falselist; }
```

If  $B_1$  is true, control can jump past the test in  $B_2$

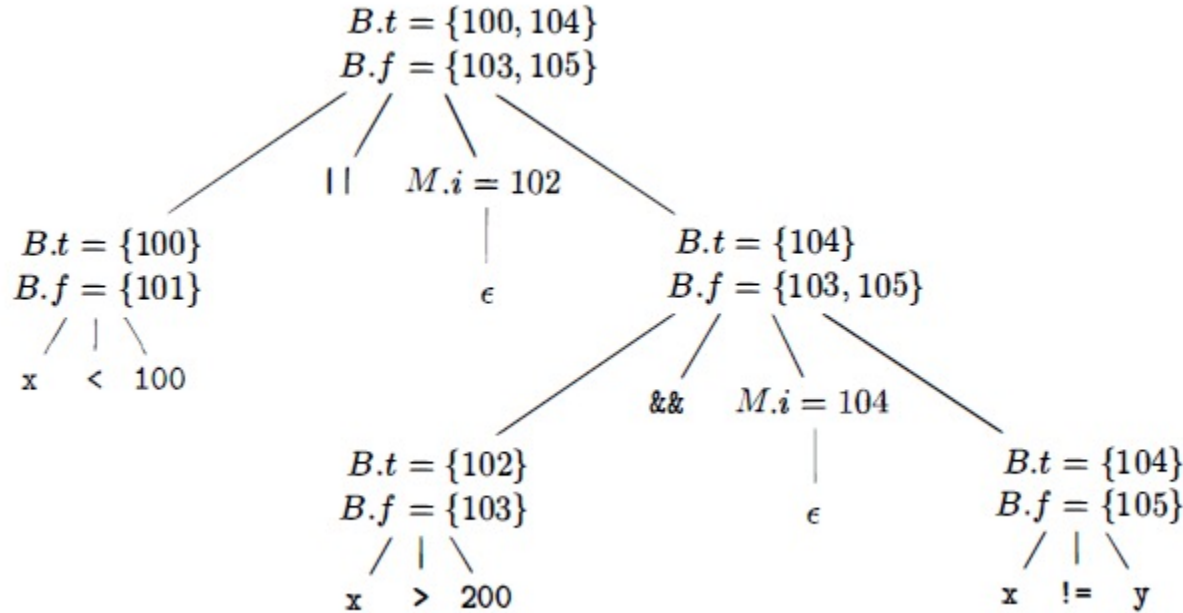
However, if it is false, it must jump to the test in  $B_2$  in order to test the complete conditional.

- So the *backpatch*() operation causes all jumps on the false list to point at  $M$  ( $M$ .*instr*) which will be the start of the code in  $B_2$
- Meanwhile, the *true*list is set to the combination of *true*lists for  $B_1$  and  $B_2$  since both of those mean the overall expression is true.
- Finally,  $B$ 's synthesized *false*list should be wherever the *false*list for  $B_2$  points.



# Backpatching Example

Parse tree for:  $x < 100 \parallel x > 200 \&\& x \neq y$



Generates:

- 100: if  $x < 100$  goto -
- 101: goto -
- 102: if  $x > 200$  goto -
- 103: goto -
- 104: if  $x \neq y$  goto -
- 105: goto -

# Backpatching Flow of Control

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- |  |  |
|--|--|
| 1) $S \rightarrow \mathbf{if}(B) M S_1$ { $backpatch(B.truelist, M.instr);$<br>$S.nextlist = merge(B.falselist, S_1.nextlist);$ }  | 4) $S \rightarrow \{ L \}$ { $S.nextlist = L.nextlist;$ }  |
| 2) $S \rightarrow \mathbf{if}(B) M_1 S_1 N \mathbf{else} M_2 S_2$<br>{ $backpatch(B.truelist, M_1.instr);$<br>$backpatch(B.falselist, M_2.instr);$<br>$temp = merge(S_1.nextlist, N.nextlist);$<br>$S.nextlist = merge(temp, S_2.nextlist);$ } | 5) $S \rightarrow A ;$ { $S.nextlist = \mathbf{null};$ }   |
| 3) $S \rightarrow \mathbf{while} M_1 (B) M_2 S_1$<br>{ $backpatch(S_1.nextlist, M_1.instr);$<br>$backpatch(B.truelist, M_2.instr);$<br>$S.nextlist = B.falselist;$<br>$emit('goto' M_1.instr);$ }  | 6) $M \rightarrow \epsilon$ { $M.instr = nextinstr;$ }   |
|  | 7) $N \rightarrow \epsilon$ { $N.nextlist = makelist(nextinstr);$<br>$emit('goto -');$ }         |
|  | 8) $L \rightarrow L_1 M S$ { $backpatch(L_1.nextlist, M.instr);$<br>$L.nextlist = S.nextlist;$ } |
|  | 9) $L \rightarrow S$ { $L.nextlist = S.nextlist;$ }  |

# Questions?

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