Communication-Efficient Matrix Multiplication on Hypercubes*

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Abstract

In this paper we present an efficient dense matrix multiplication algorithm for distributed memory computers with a hypercube topology. The proposed algorithm performs better than all previously proposed algorithms for a wide range of matrix sizes and number of processors, especially for large matrices. We analyze the performance of the algorithms for two types of hypercube architectures, one in which each node can use (to send and receive) at most one communication link at a time and the other in which each node can use all communication links simultaneously.

Keywords Matrix multiplication, distributed algorithms, interprocessor communication, hypercubes, 3-D grids.

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